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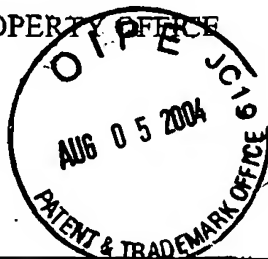
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31561 7590 06/17/2004

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(Signature)

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/605,360	09/25/2003	ERH-KUN LAI	11760-US-PA	2359

TITLE OF INVENTION: [METHOD FOR FABRICATING A MOSFET AND REDUCING LINE WIDTH OF GATE STRUCTURE]

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	09/17/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
KESHAVAN, BELUR V	2825	438-197000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

MACRONIX International CO., Ltd.

Hsinchu, Taiwan, R.O.C.

Please check the appropriate assignee category or categories (will not be printed on the patent):

☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

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☒ Publication Fee
☐ Advance Order - # of Copies _____

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08/05/2004 HDEMSS2 00000011 502620 10605360

01 FC:1501

1330.00 DA

02 FC:1504

300.00 DA



Patent

Customer No.: 31561
Docket No. 11760-US-PA
Application No.: 10/605,360

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Applicant : Lai
Application No. : 10/605,360
Filed : 2003/9/25
For : METHOD FOR FABRICATING A MOSFET AND
REDUCING LINE WIDTH OF GATE STRUCTURE
Art Unit : 2825
Examiner : KESHAVAN, BELUR V

TRANSMITTAL LETTER

002-1-703-746-4000

(Via fax: 4 pages, followed by confirmation copy via courier)

Assistant Commissioner for Patents
Arlington, Virginia 22202

Dear Sirs,

In response to the Notice of Allowance and Fee(s) due, dated June 17, 2004, attached herewith please find the relevant paper. Following the fax transmission, a hard copy via courier will also be forwarded to the Office.

Enclosed documents via courier will include:

- ☒ Issue Fee Transmittal (PTOL-85B) in duplicate
- ☒ Prepaid return postcard
- ☒ Fax confirmation report

Please charge the fee in the amount of \$1630.00 for Issue Fee and Publication Fee to Account No. 50-2620 (Order No.: 11760-US-PA).

Thank you for your assistance in the subject matter. If you have any questions, please feel free to contact me.



Patent

Customer No.: 31561
Docket No. 11760-US-PA
Application No.: 10/605,360

Respectfully Submitted,
JIANQ CHYUN Intellectual Property Office

Date: August 5, 2004

By: Belinda Lee
Belinda Lee
Registration No.: 46,863

Please send future correspondence to:
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